

PTO/SB/08a Substitute for Form 1449A/PTO		Application Number	Not Yet Assigned
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		Filing Date	Herewith
		First Named Inventor	Otomo et al.
		Art Unit	Not Yet Assigned
		Examiner Name	Not Yet Assigned
Sheet	1 of 1	Attorney Docket	85366

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/KA/		US-6,225,831 B1	05/01/2001	Dalmia et al.	
/KA/		US-6,421,404 B1	07/16/2002	Nakamura	
/KA/		US-2003/0142774 A1	07/31/2003	Takasoh et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
/KA/		JP2000077990	03/14/2000	Victor Co. of Japan Ltd.		X
/KA/		JP2001144592	05/25/2001	Fujitsu Ltd.		X
/KA/		JP2002171160	06/14/2002	Nec Eng Ltd.		X

NON PATENT LITERATURE DOCUMENTS			
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/KA/		NAKAMURA, KAZUYUKI; FUKAISHI, MUNEO; ABIKO, HITOSHI; MATSUMOTO, AKIRA and YOTSUYANAGI, MICHIO, A 6 Gbps CMOS Phase Detecting DEMUX Module Using Half-Frequency Clock, IEEE, 1998	
/KA/		SAVOJ, JAFAR and RAZAVI, BEHZAD, A 10-Gb/s CMOS Clock and Data Recovery Circuit with a Half-Rate Linear Phase Detector, IEEE Journal of Solid-State Circuits, Vol. 36, No. 5, May 2001, pages 761-768.	
/KA/		OHTOMO, YUSUKE; KAWAMURA, TOMOAKI; NISHIMURA, KAZUYOSHI, NOGAWA, MASAFUMI; KOIZUMI, HIROSHI and TOGASHI, MINOURU, A 12.5 Gb/s CMOS BER Test Using A Jitter-Tolerant Parallel CDR, IEEE International Solid-State Circuits Conference, ISSCC Digest of Technical Papers, Feb. 2004, pages 174-175.	

Examiner Signature	/Khareem Almo/	Date Considered	07/31/2007
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2126/07

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10535273
	Filing Date		2005-05-17
	First Named Inventor	Otomo et al.	
	Art Unit	2816	
	Examiner Name	Khareem E. Almo	
	Attorney Docket Number	85366	

## U.S.PATENTS

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/KA/	1	11-112335	JP	A	1999-04-23	NEC Corp.		<input type="checkbox"/>
/KA/	2	11-355133	JP	A	1999-12-24	NEC Corp.		<input type="checkbox"/>
/KA/	3	2001-196907	JP	A	2001-07-19	NEC Eng Ltd		<input type="checkbox"/>

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STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number	10535273
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First Named Inventor	Otomo et al.
Art Unit	2816
Examiner Name	Khareem E. Almo
Attorney Docket Number	85366

/KA/	4	2002-314387	JP	A	2002-10-25	Nippon Telegraph and Telephone Corp.	<input type="checkbox"/>
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